

What is claimed is:

1. An in-plane switching mode liquid crystal display device comprising:
first and second substrates having an array region and a sealant region along a periphery of the array region;
a sealant in the sealant region attaching the first and second substrates;
a metallic black matrix formed in the sealant region and in the array region of first substrate;
a color filter on the metallic black matrix;
an organic layer on the color filter; and
a liquid crystal layer between the first and second substrates.
2. The device of claim 1, wherein the metallic black matrix is one of Cr and CrO_x.
3. The device of claim 1, wherein the organic layer is formed in the array region.
4. The device of claim 3, wherein the organic layer is in direct contact with the metallic black matrix.
5. The device of claim 1, wherein the organic layer is formed in the array region and in the sealant region.
6. The device of claim 5, wherein the organic layer is in direct contact with the sealant.
7. The device of claim 1, wherein the second substrate comprises:
gate lines and data lines arranged vertically and horizontally to define a pixel region;
a thin film transistor adjacent each crossing of the gate and data lines;

a gate pad and a data pad at an end of the gate and data lines; and
a common electrode and a pixel electrode in the pixel region.

8. A method for fabricating an in-plane switching mode liquid crystal display device, comprising:
- providing first and second substrates having a sealant region and an array region;
 - forming a metallic black matrix in the sealant region and in the array region of the first substrate;
 - forming a color filter on the metallic black matrix;
 - forming an organic layer on the color filter;
 - forming a sealant in the sealant region; and
 - attaching the first and second substrates by the sealant.
9. The method of claim 8, wherein the metallic black matrix is one of Cr and CrO_x .
10. The method of claim 8, wherein the organic layer is formed in the array region.
11. The method of claim 8, wherein the organic layer is formed in the sealant region and the array region.
12. The method of claim 8, further comprising:
- forming a thin film transistor, a pixel electrode and a common electrode on the second substrate.
13. The method of claim 8, further comprising:
- forming a liquid crystal layer between the first and second substrates.